

L Number	Hits	Search Text	DB	Time stamp
-	37	netlist and prior near3 circuit and physical adj design	USPAT	2003/04/28 14:10
-	0	netlist and current adj integrated adj circuit same prior adj integrated adj circuit	USPAT	2002/09/09 11:02
-	0	netlist and current adj integrated adj circuit and prior adj integrated adj circuit	USPAT	2002/09/09 11:05
-	0	netlist and current adj integrated adj circuit same prior adj integrated adj circuit	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/09 10:57
-	9	netlist and prior near3 circuit and physical adj design	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/09 10:58
-	3	netlist and current near3 circuit same prior near3 circuit	USPAT	2002/09/09 11:03
-	3	netlist and prior adj integrated adj circuit	USPAT	2002/09/09 13:03
-	8	physical adj design same netlist and (prior or former or previous) near3 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/09 13:10
-	54	netlist and current near3 circuit and (prior or former or previous) near3 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/09 14:02
-	30	layout near3 design near3 information and netlist	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/10 10:39
-	29	physical near3 design near3 information and netlist	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/10 12:24
-	390	physical near3 design and netlist	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/11 08:55
-	71	gds adj II	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/11 14:58
-	91	netlist\$1 same logic adj design	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/11 15:47
-	1	("6425113").PN.	USPAT	2002/09/13 13:30
-	1	("6442745").PN.	USPAT	2002/09/13 13:33
-	14	netlist same pin adj assignment\$1	USPAT	2002/09/13 15:12
-	1	((("6009251").PN.) and pin\$1	USPAT	2002/09/13 15:16
-	0	physical adj design adj information same pin adj assignment\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/28 13:49

-	0	physical adj design adj information same clock adj distribution adj tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/28 13:47
-	1	physical adj design same clock adj distribution adj tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/28 14:38
-	2	physical adj design same pin adj assignment\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/28 13:51
-	6	physical adj design same parasitic adj extraction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/28 13:51
-	1	(layout or physical) adj design same clock adj distribution adj tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/28 14:42
-	21	net adj list and prior near3 circuit and physical adj design	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 14:13
-	0	net adj list and prior near3 circuit with physical adj design adj information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 14:23
-	3	netlist with physical adj design adj information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:00
-	6	netlist with physical adj design near3 information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:01
-	2	prior same physical adj design adj information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:01
-	0	history same physical adj design adj information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:03
-	19	prior and physical adj design adj information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:11
-	29	physical adj design adj phase	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:24
-	234	layout adj design and netlist	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/28 15:25